MT3274S

N-Channel Power MOSFET

40 V, 70 A,5.4 mΩ

Features

- Typ R_{DS} (on)= 5.4mΩ at V_{GS} =10V
- High performance trench technology for extremely low R_{DS} (on)
- High power and current handing capability

General Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low R_{DS}(ON) and fast switching speed.

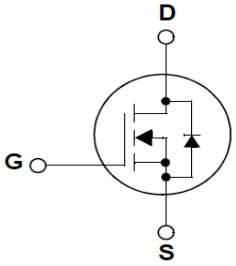
Applications

- DC-DC primary bridge
- DC-DC Synchronous rectification
- Power Managemement for Inverter Systems

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Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



TO-252-2L

Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (T_A=25°C Unless Otherwise Noted)				
V_{DSS}	Drain-Source Voltage		40	V
V_{GSS}	Gate-Source Voltage		±20	
T_J	Maximum Junction Temperature		150	°C
T_{STG}	Storage Temperature Range		-55 to 150	°C
I_S	Diode Continuous Forward Current	T_C=25°C	70	A
Mounted on Large Heat Sink				
I_{DM}	Pulsed Drain Current *		310	A
I_D	Continuous Drain Current	T_C=25°C	70	A
		T_C=100°C	41	
P_D	Maximum Power Dissipation	T_C=25°C	55	W
		T_C=100°C	28	
$R_{\theta JC}$	Thermal Resistance-Junction to Case		1.6	°C/W

Electrical Characteristics (T_A = 25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_{DS}=250μA	40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=24V, V_{GS}=0V	-	-	1	μA
		T_J=85°C	-	-	10	
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=250μA	1	1.6	3	V
I_{GSS}	Gate Leakage Current	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
$R_{DS(ON)}$*	Drain-Source On-state Resistance	V_{GS}=10V, I_{DS}=20A	-	5.4	8.5	mΩ
Diode Characteristics						
V_{SD}*	Diode Forward Voltage	I_{SD}=20A, V_{GS}=0V	-	0.8	1.3	V
t_{rr}	Reverse Recovery Time	I_{SD}=20A, dI_{SD}/dt=100A/μs	-	13	-	ns
Q_{rr}	Reverse Recovery Charge		-	12	-	nC

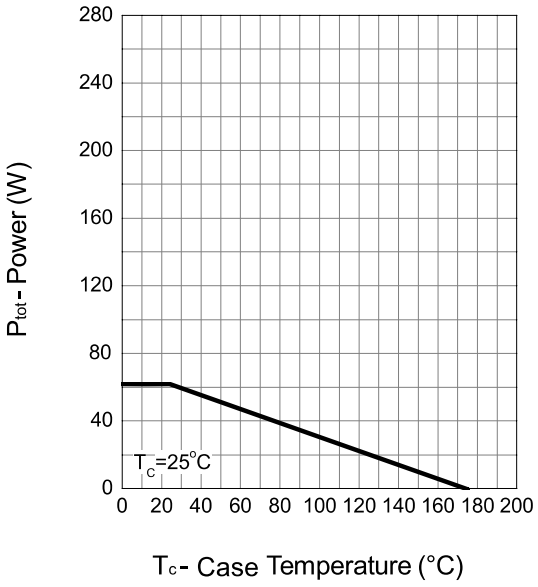
Electrical Characteristics (Cont.) (T_A = 25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Dynamic Characteristics						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V,F=1MHz	-	3.0	-	Ω
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=20V, Frequency=1.0MHz	-	3310	-	pF
C_{oss}	Output Capacitance		-	257	-	
C_{rss}	Reverse Transfer Capacitance		-	186	-	
$t_{d(ON)}$	Turn-on Delay Time	V_{DD}=20V, R_G=3 Ω, I_{DS}=30A, V_{GS}=10V,	-	13	-	ns
T_r	Turn-on Rise Time		-	30	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	62	-	
T_f	Turn-off Fall Time		-	18	-	
Gate Charge Characteristics						
Q_g	Total Gate Charge	V_{DS}=15V, V_{GS}=20V, I_{DS}=30A	-	71	-	nC
Q_{gs}	Gate-Source Charge		-	16	-	
Q_{gd}	Gate-Drain Charge		-	17	-	

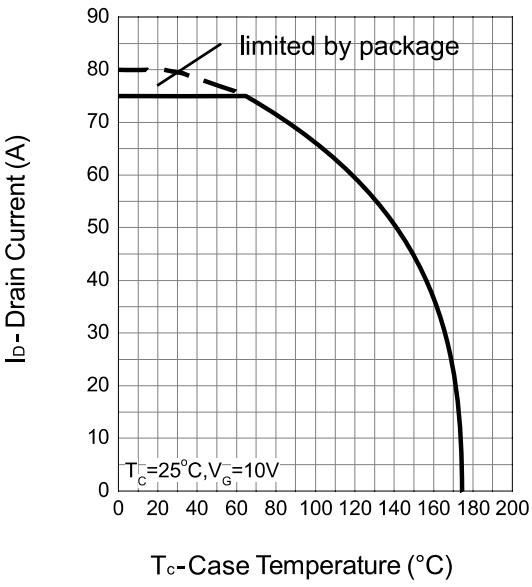
Note * : Pulse test ; pulse width ≤300μs, duty cycle≤2%.

Typical Operating Characteristics

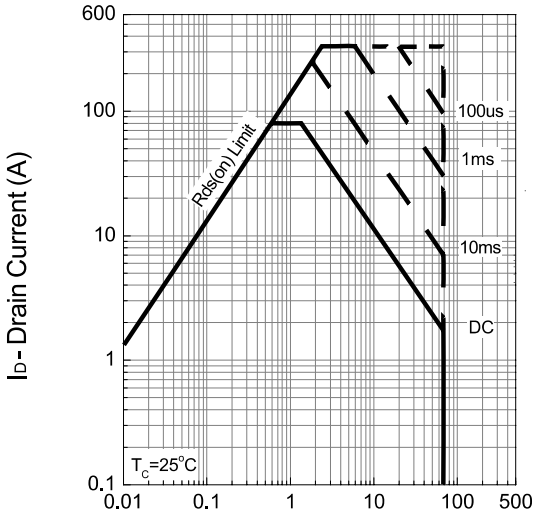
Power Dissipation



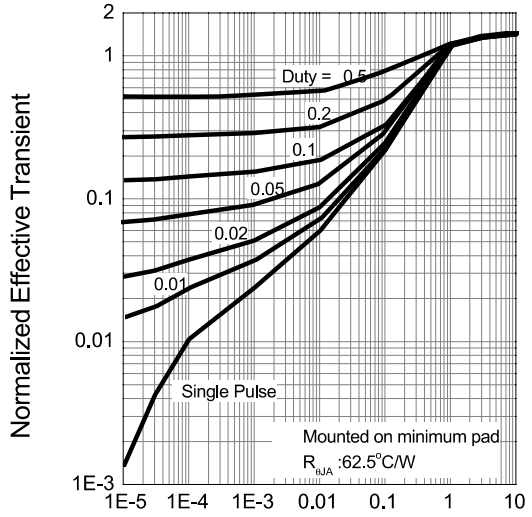
Drain Current



Safe Operation Area

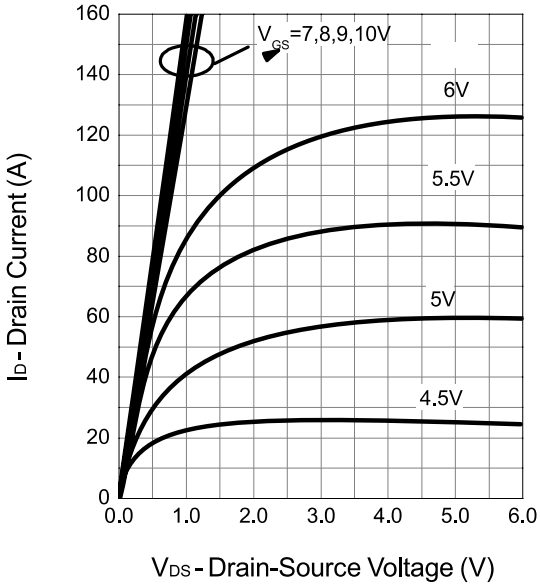


Thermal Transient Impedance

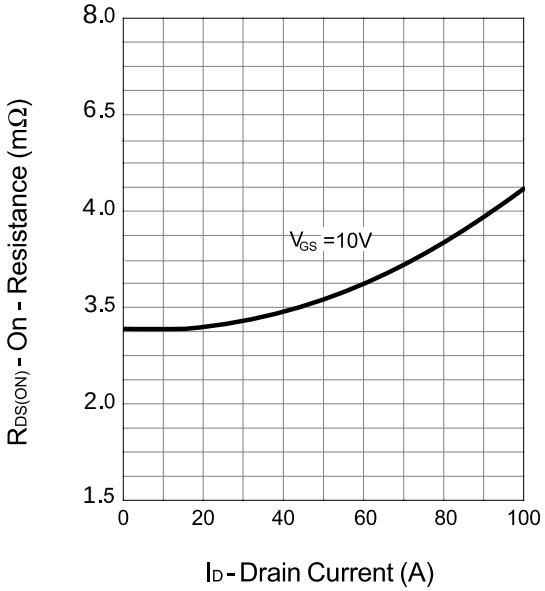


Typical Operating Characteristics (Cont.)

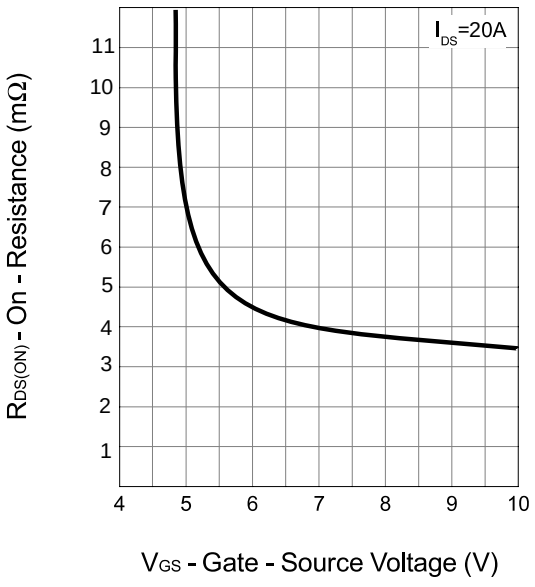
Output Characteristics



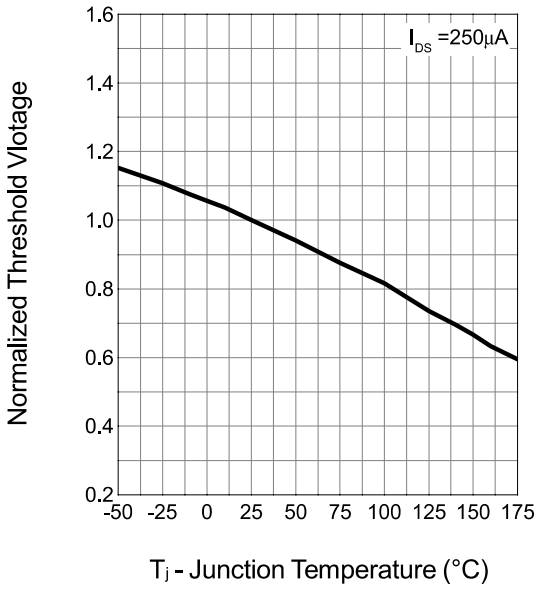
Drain-Source On Resistance



Drain-Source On Resistance

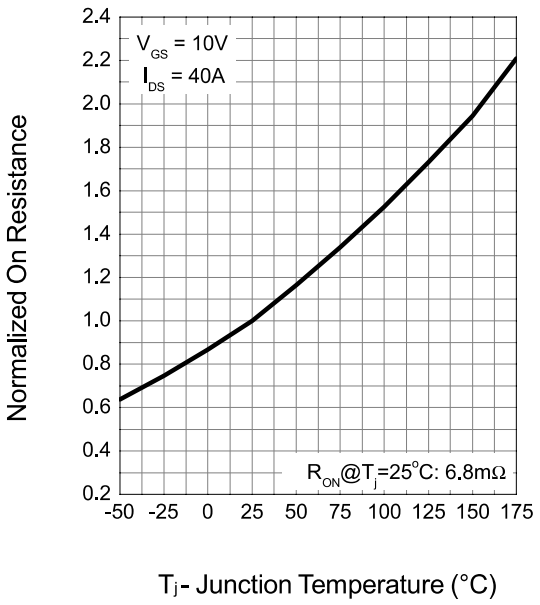


Gate Threshold Voltage

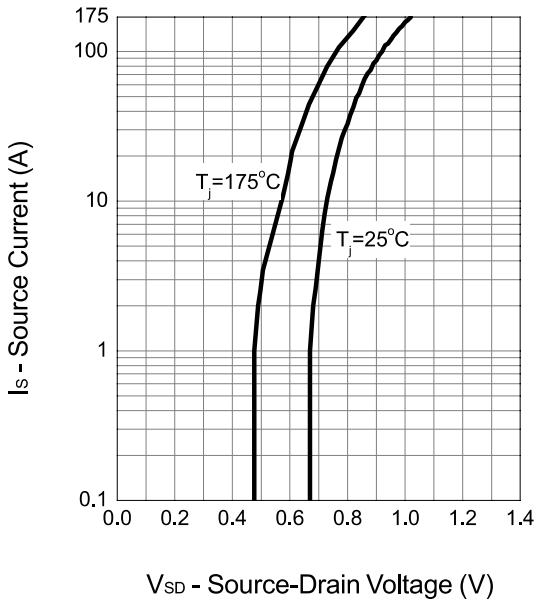


Typical Operating Characteristics (Cont.)

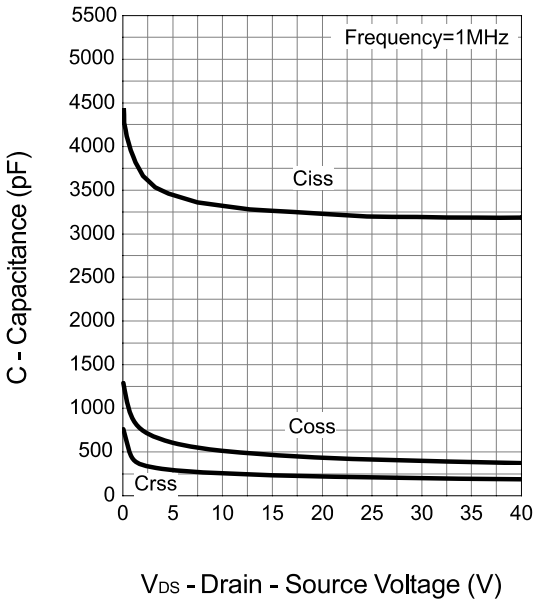
Drain-Source On Resistance



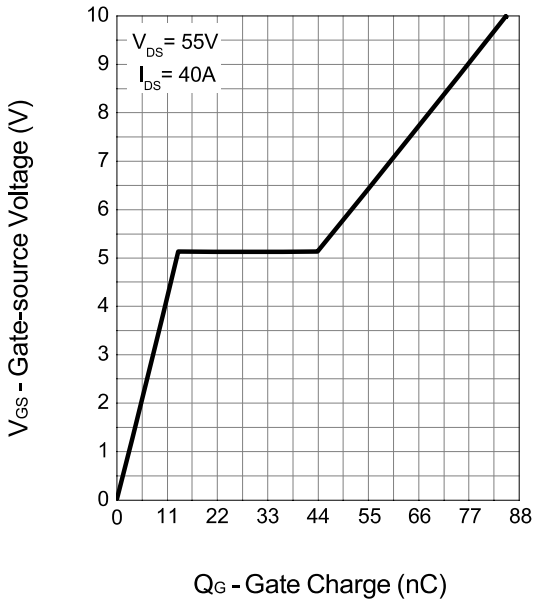
Source-Drain Diode Forward



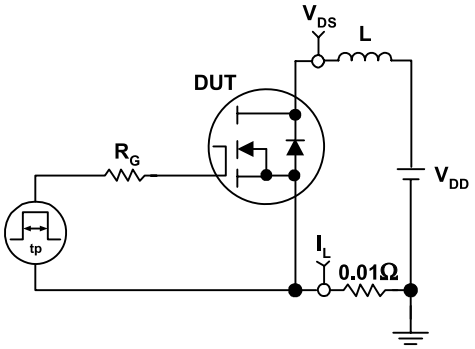
Capacitance

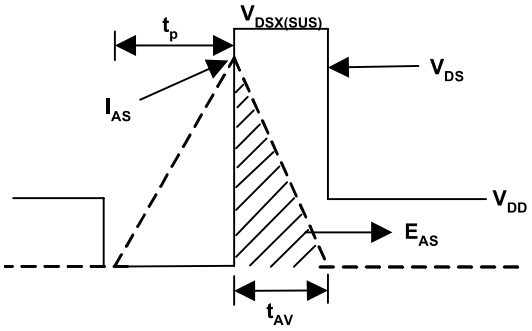


Gate Charge

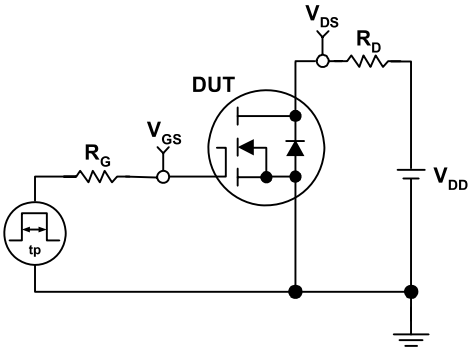


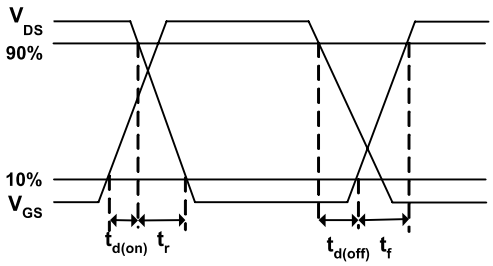
Avalanche Test Circuit and Waveforms





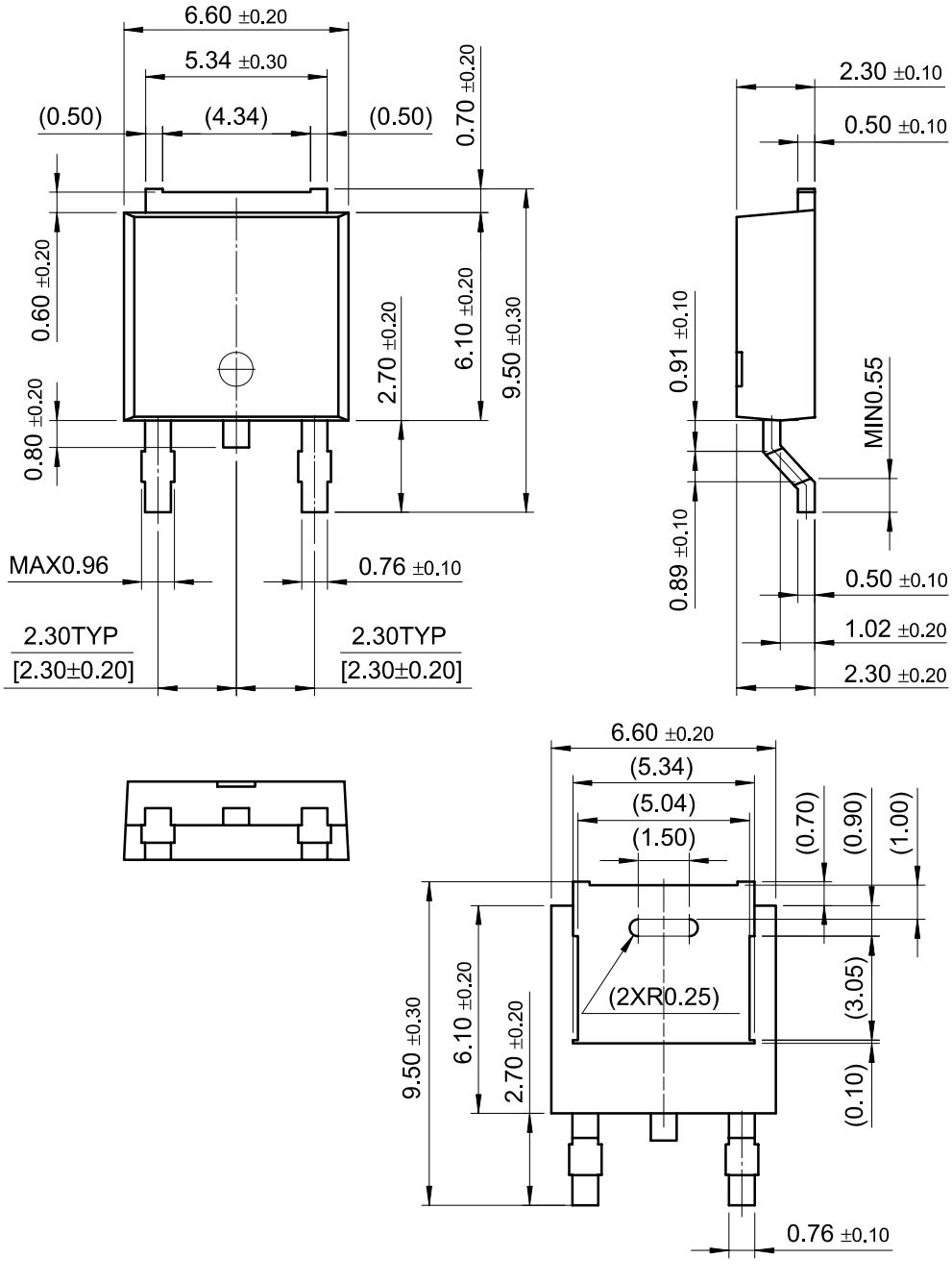
Avalanche Test Circuit and Waveforms





Package Dimensions

TO-252-2L



Dimensions in Millimeters

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